

FIG. 1

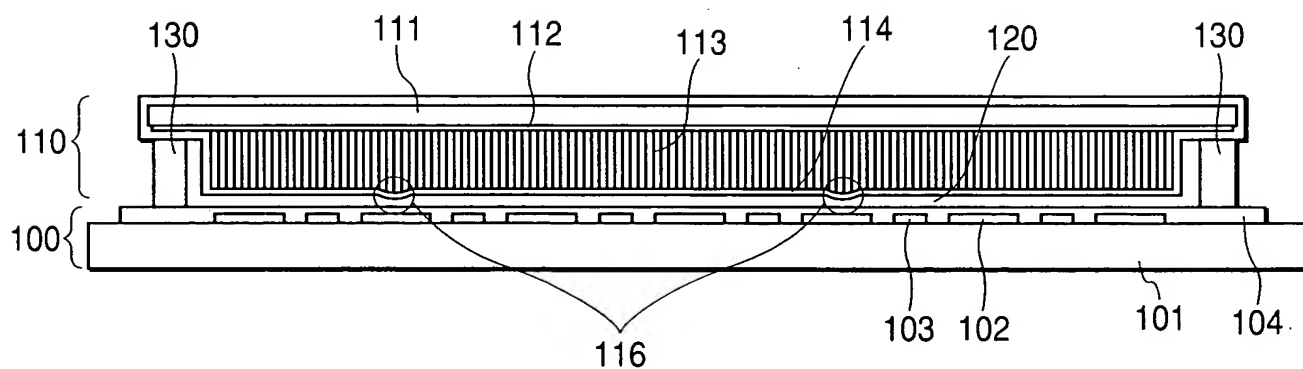
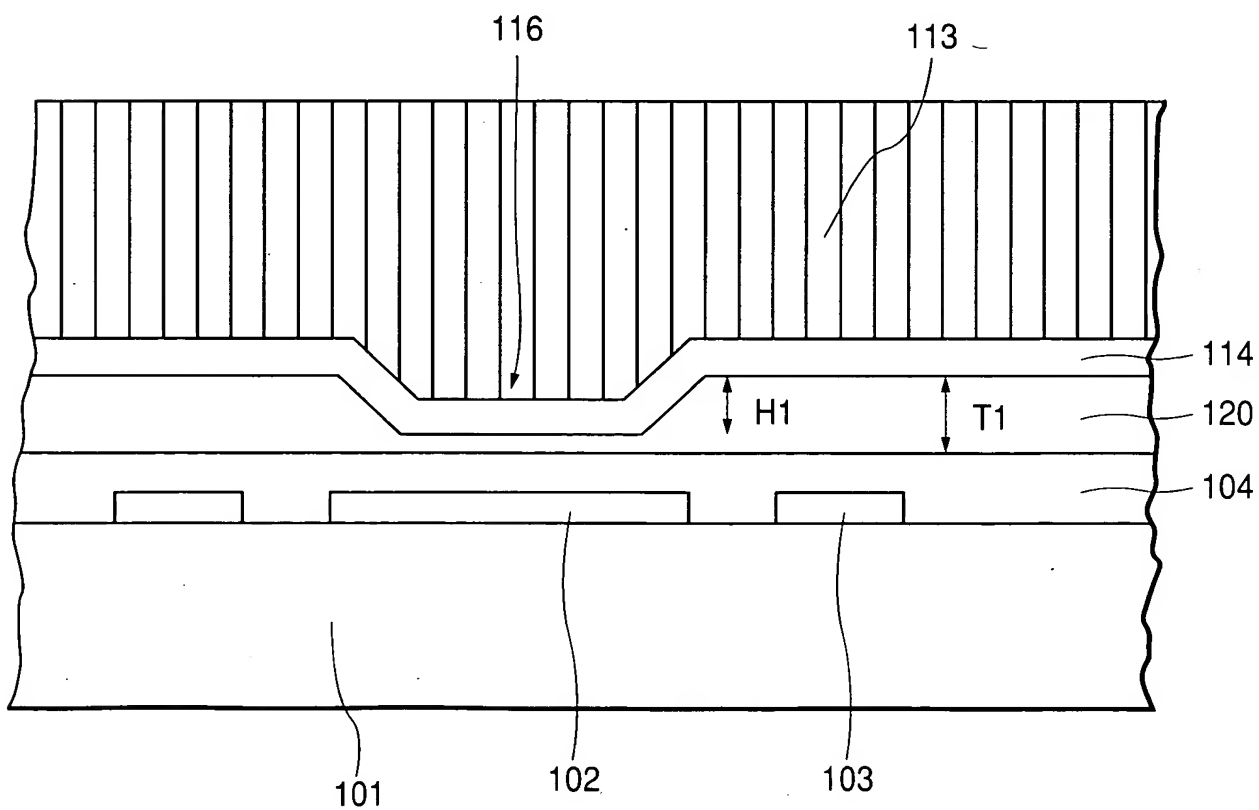


FIG. 2



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FIG. 3A

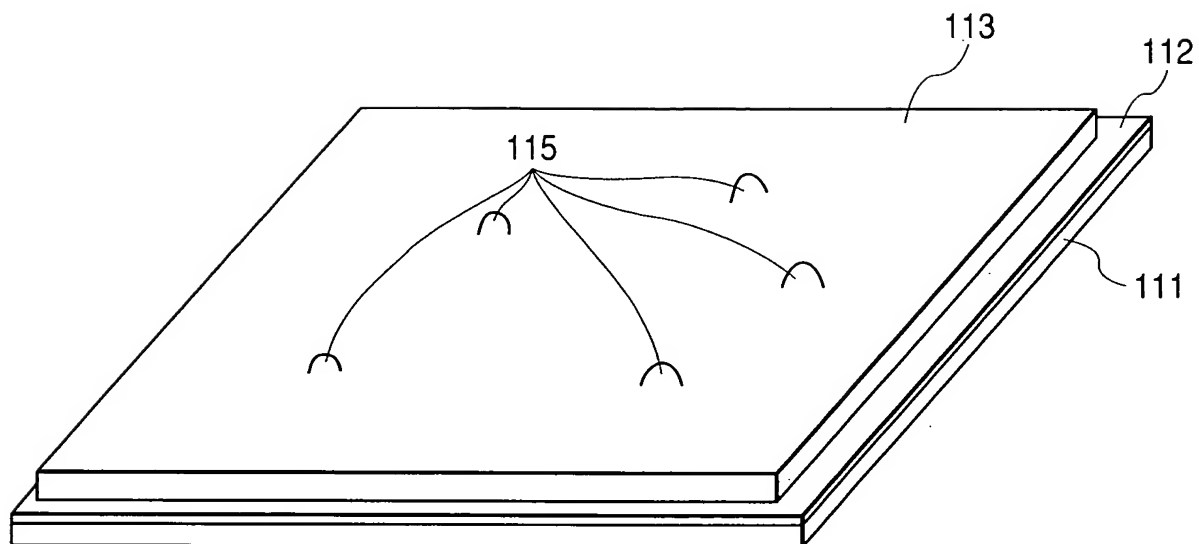


FIG. 3B

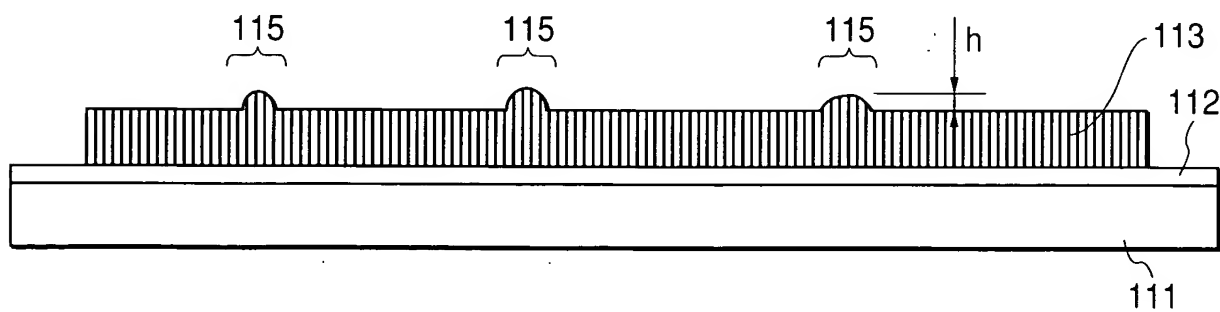


FIG. 4A

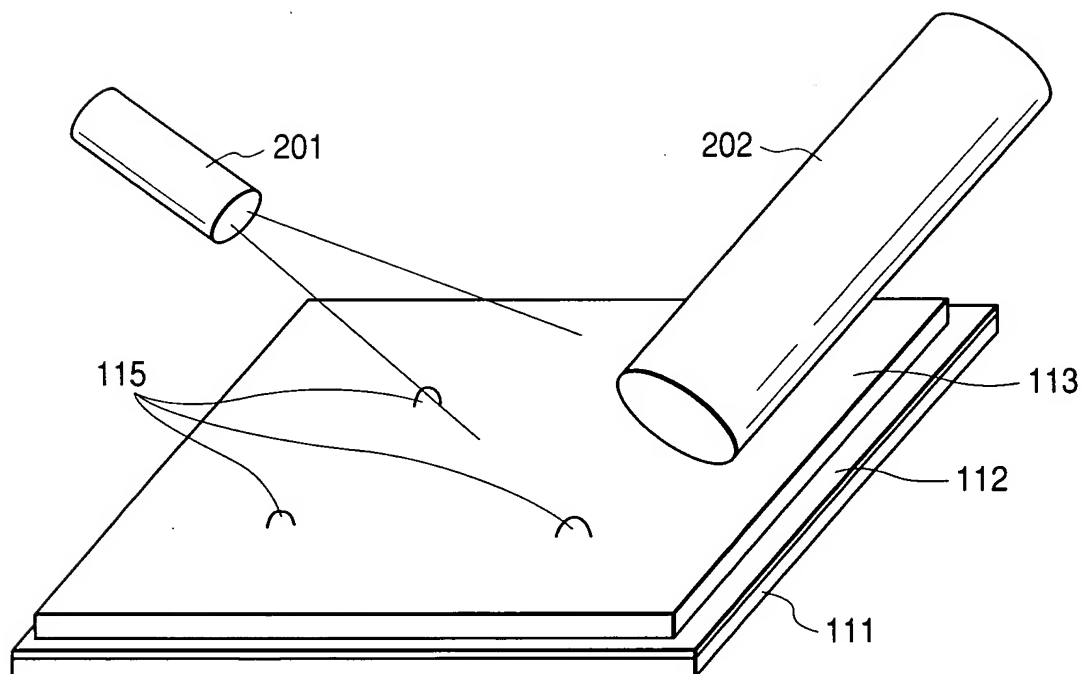


FIG. 4B

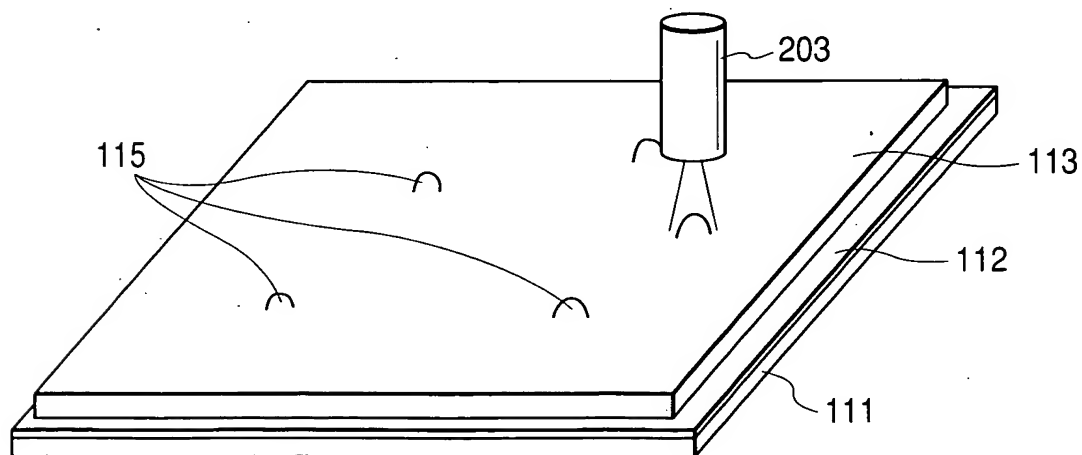


FIG. 5A

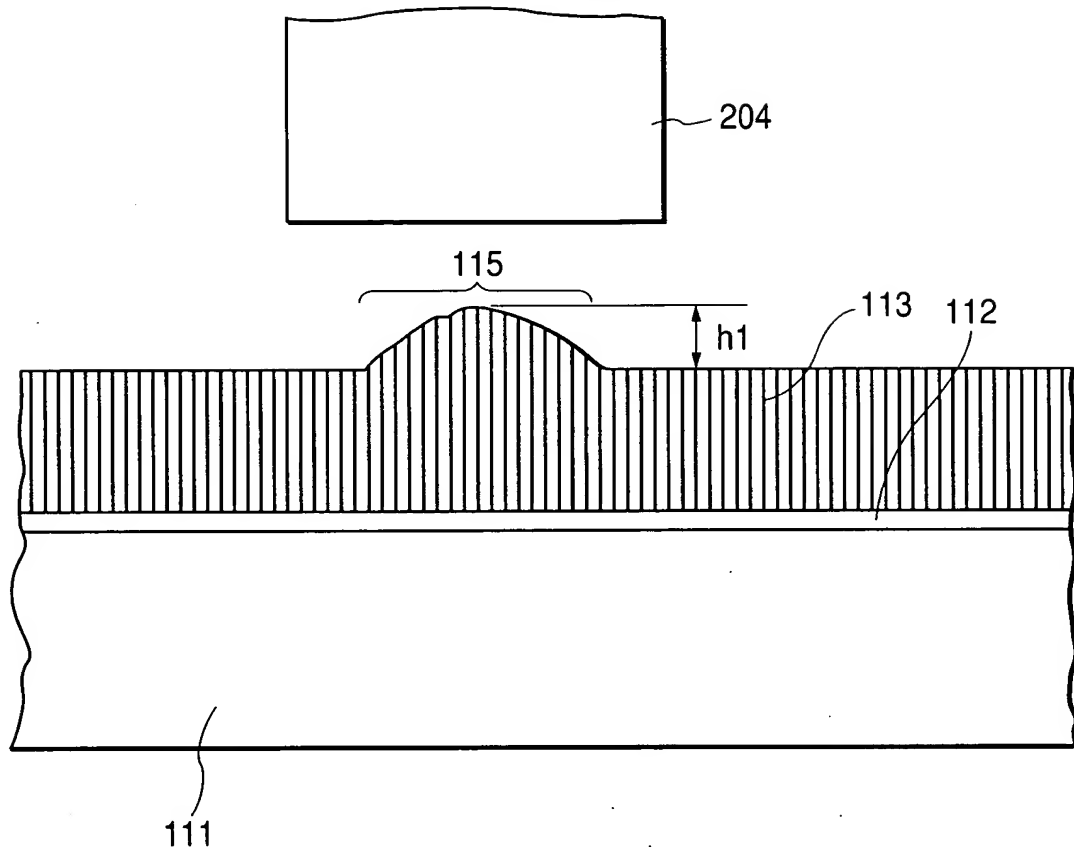


FIG. 5B

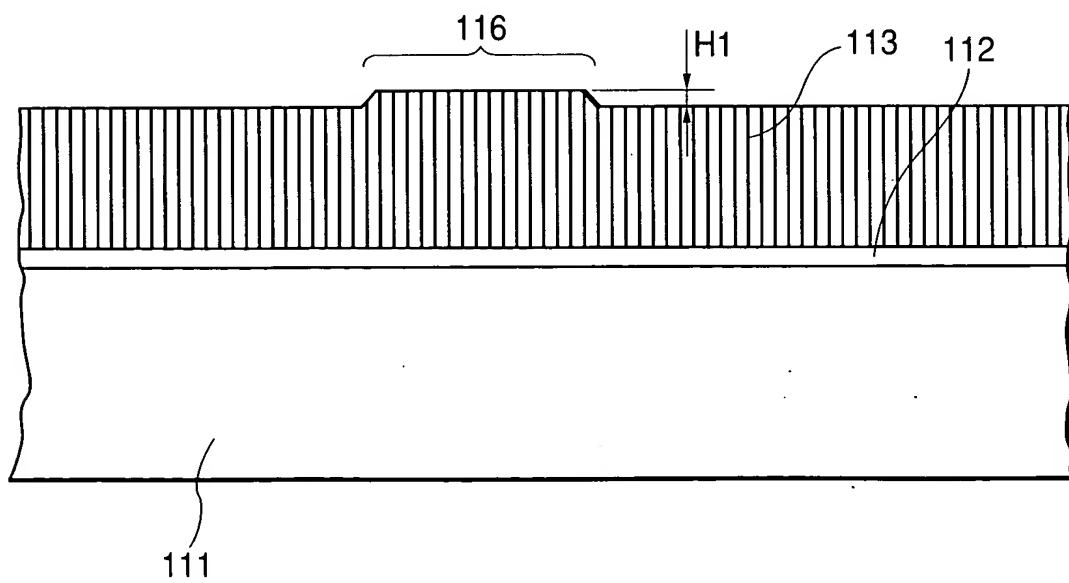


FIG. 6A

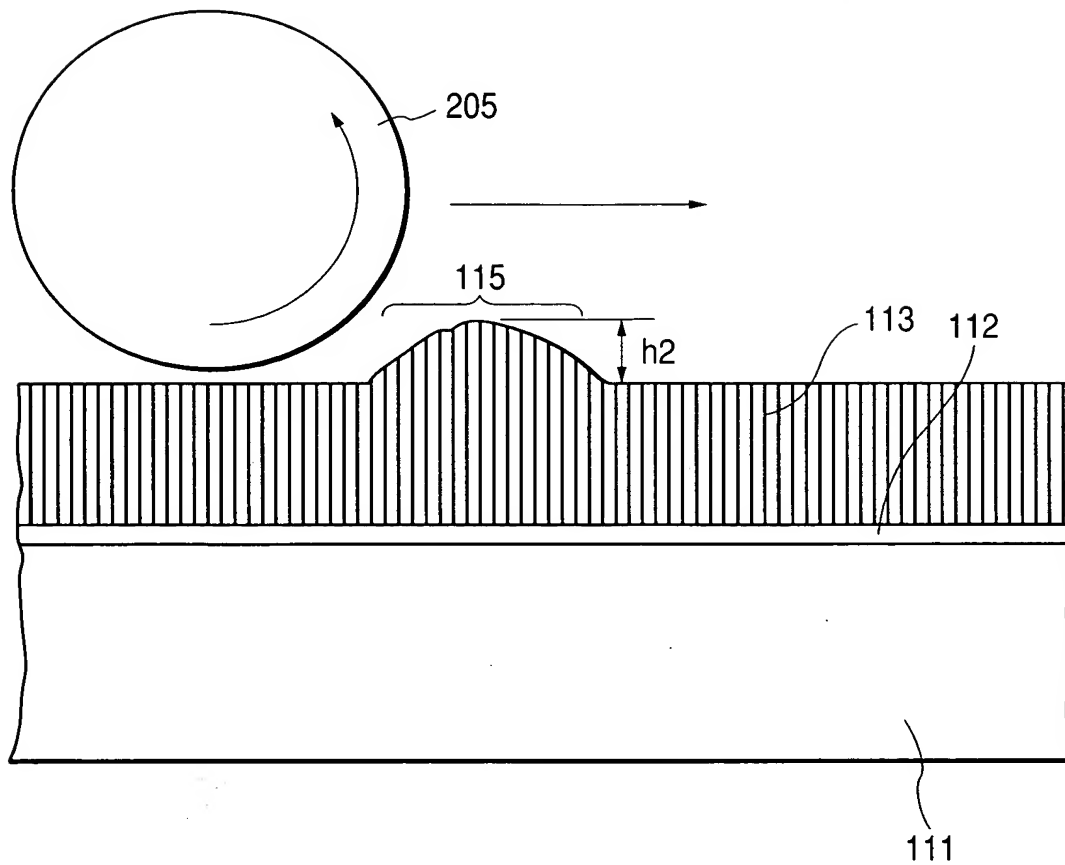


FIG. 6B

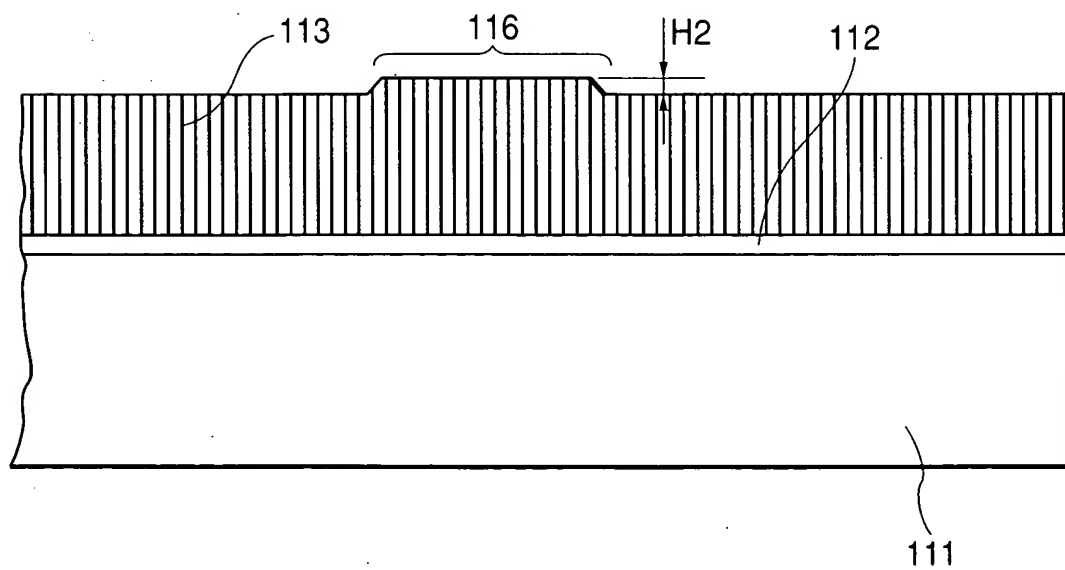


FIG. 7A

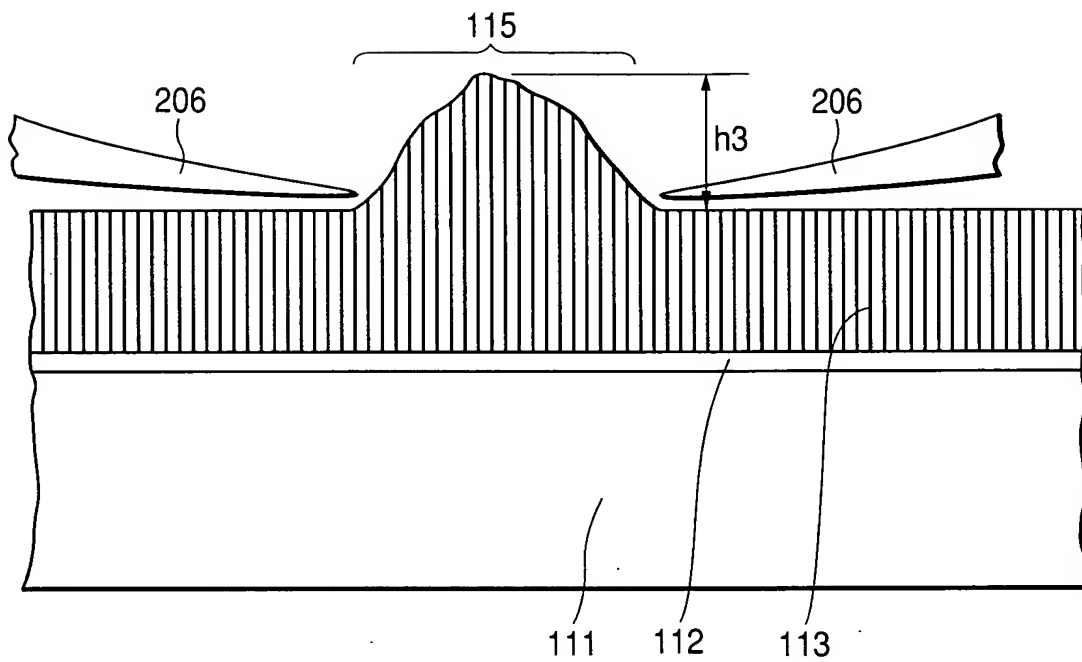


FIG. 7B

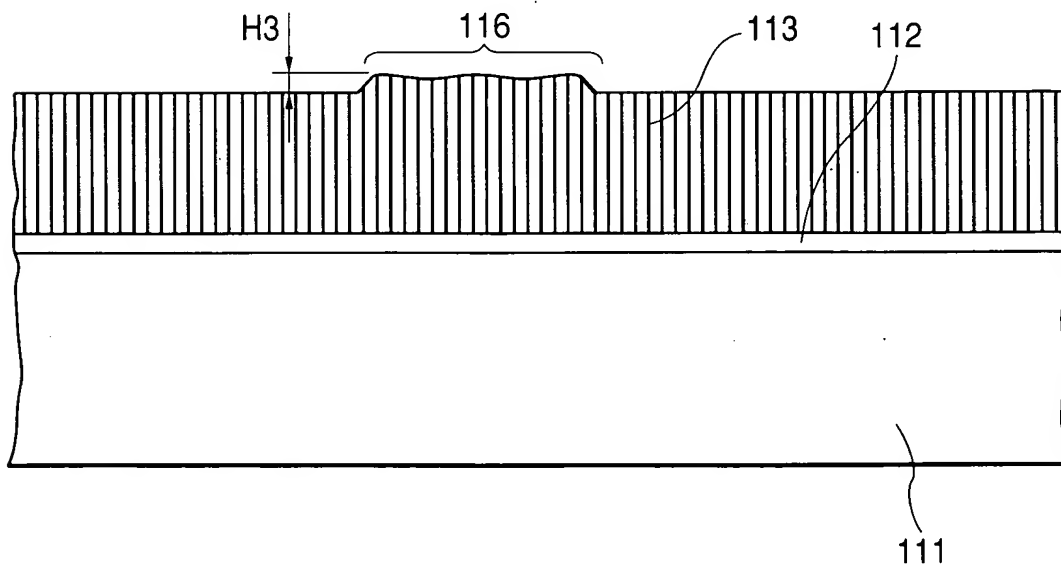


FIG. 8A

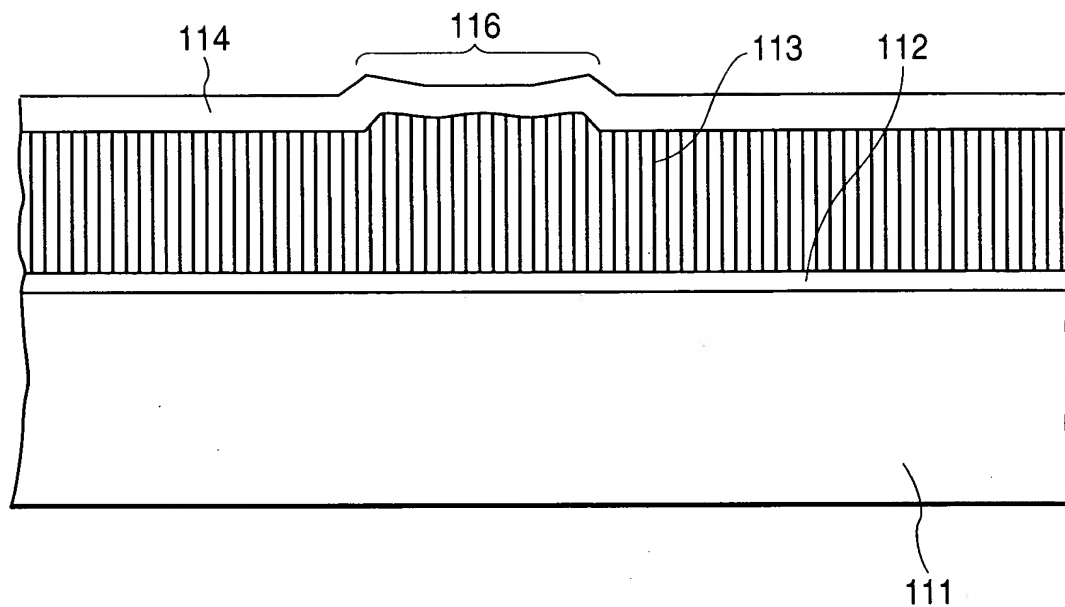


FIG. 8B

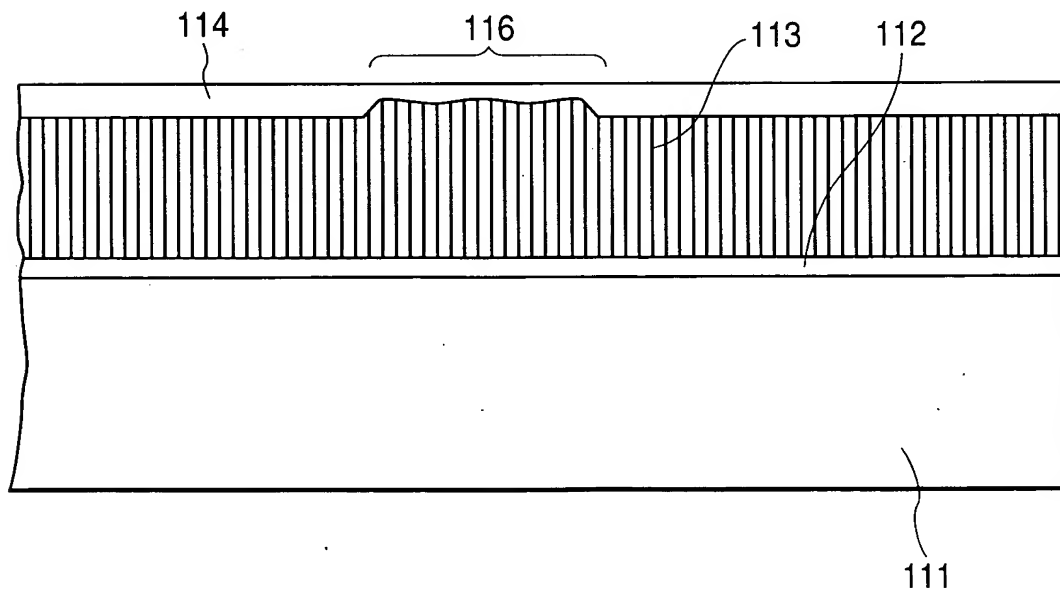


FIG. 9A

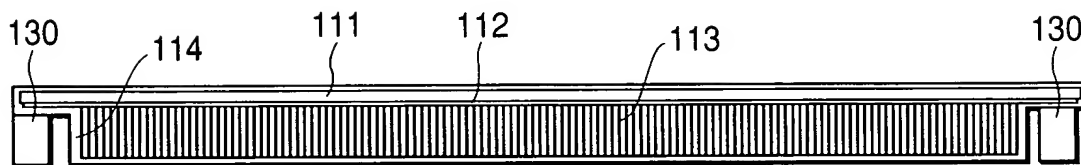
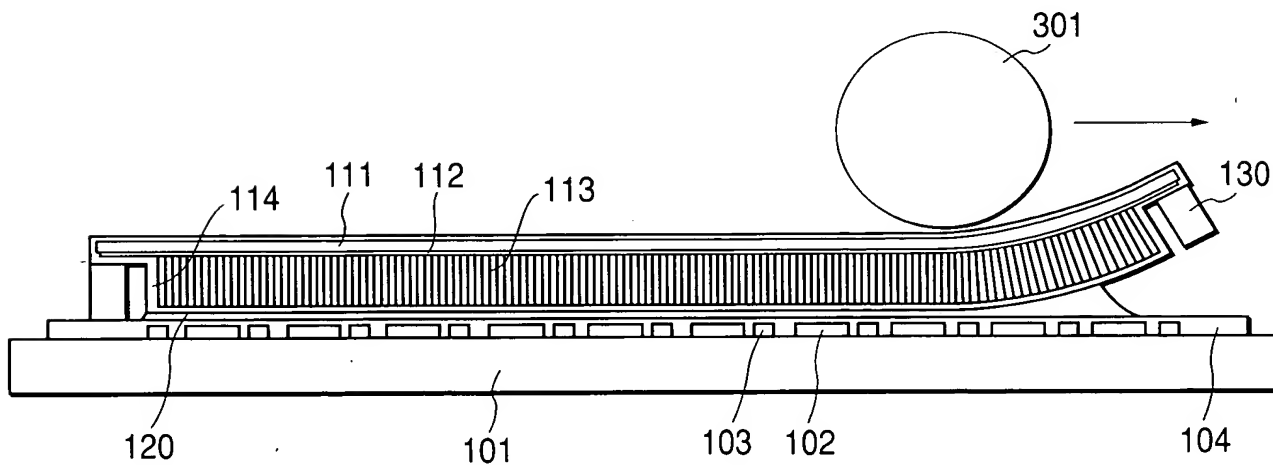


FIG. 9B



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FIG. 10

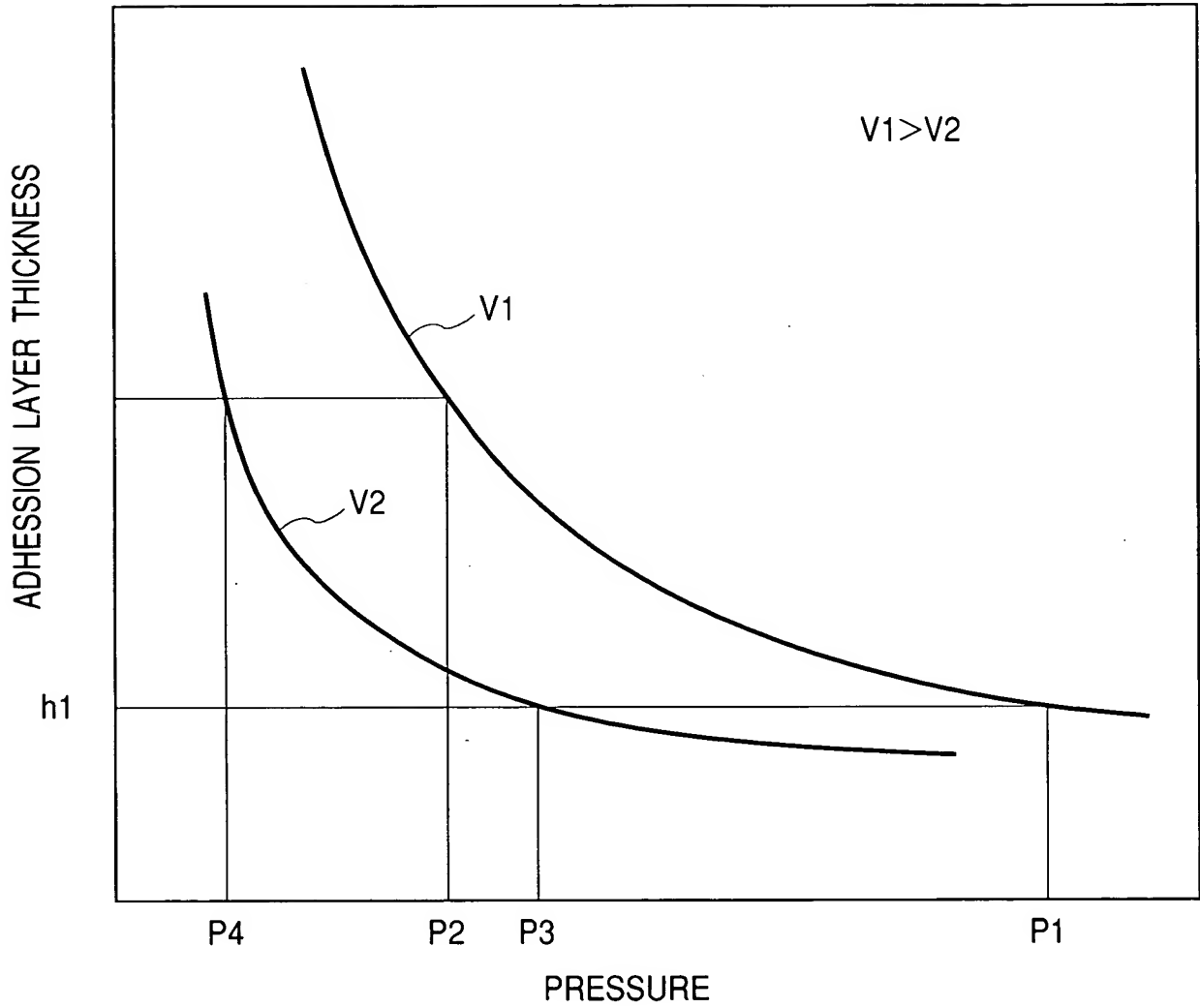


FIG. 11

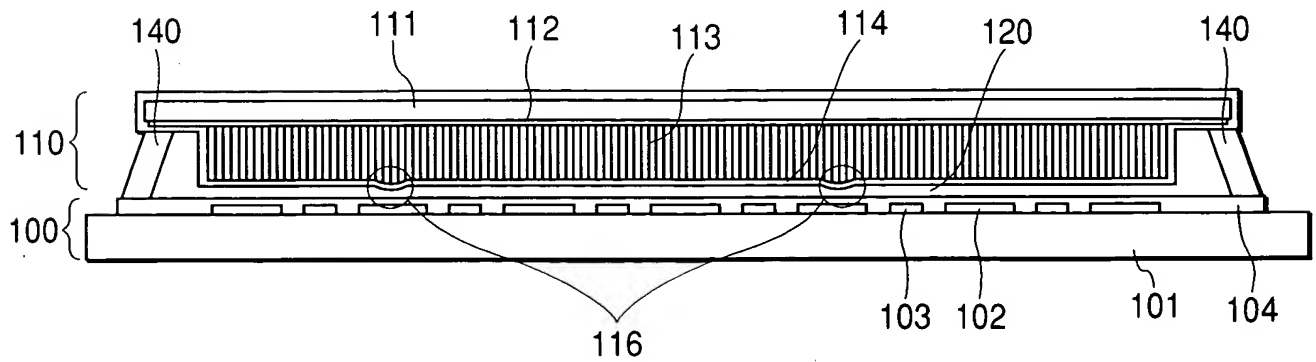


FIG. 12

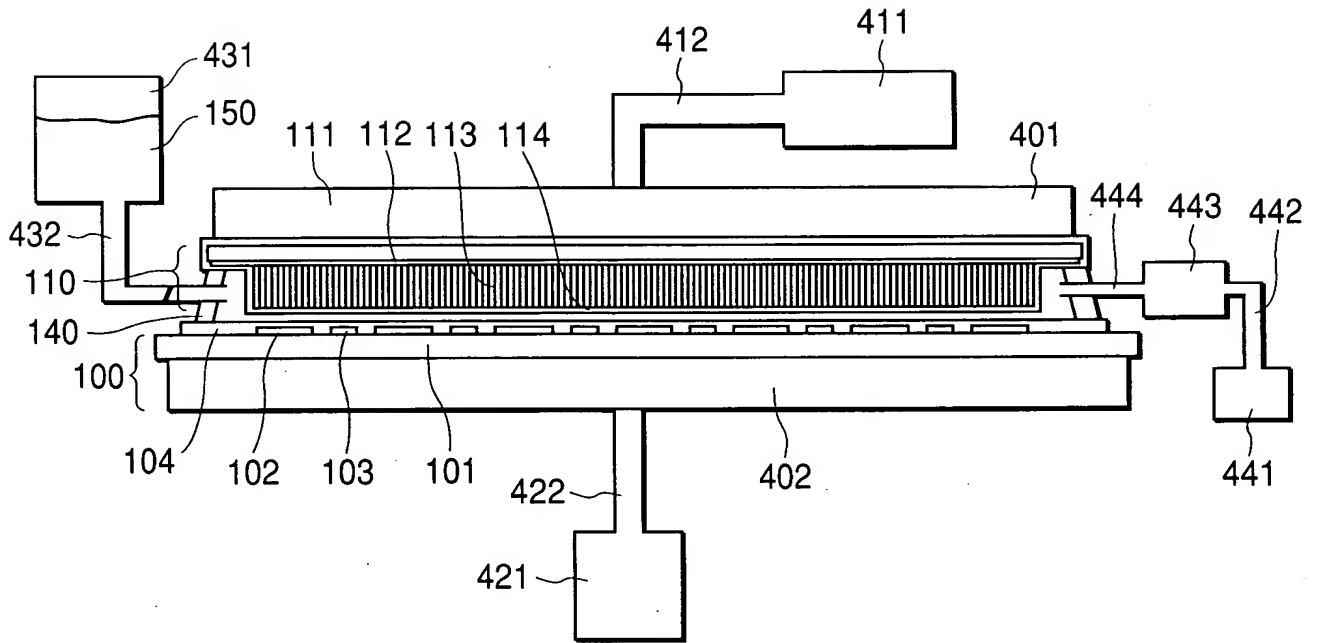


FIG. 13

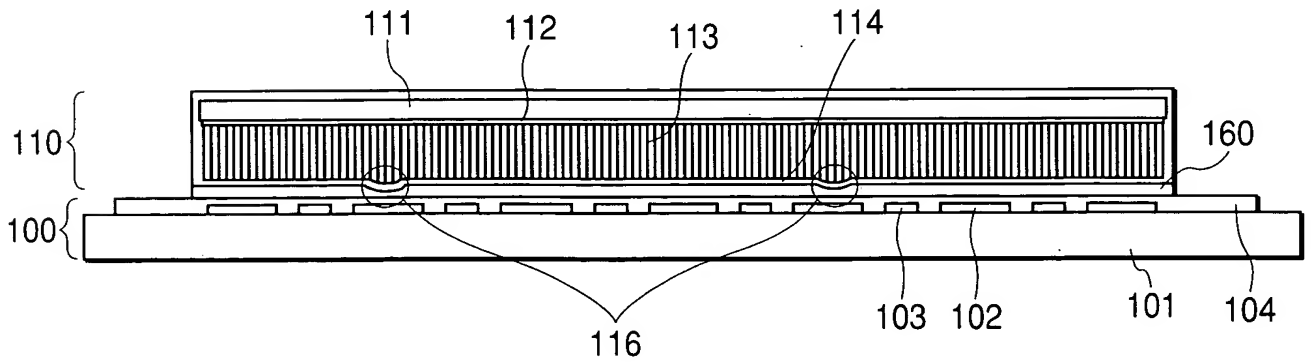


FIG. 14A

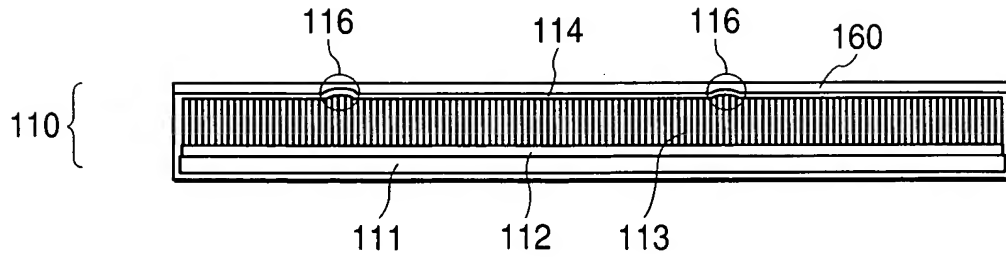


FIG. 14B

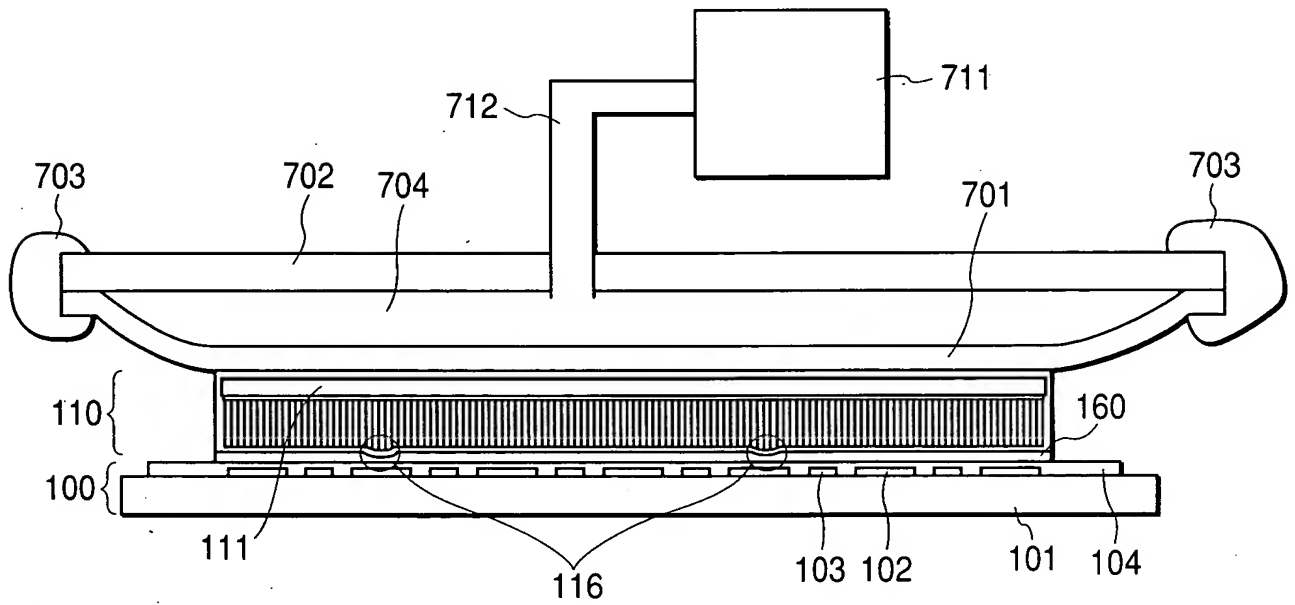


FIG. 15

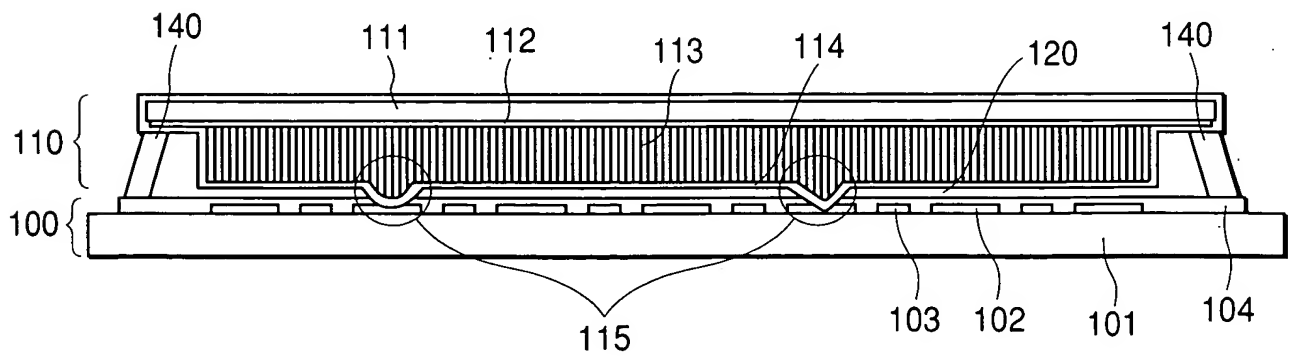


FIG. 16A

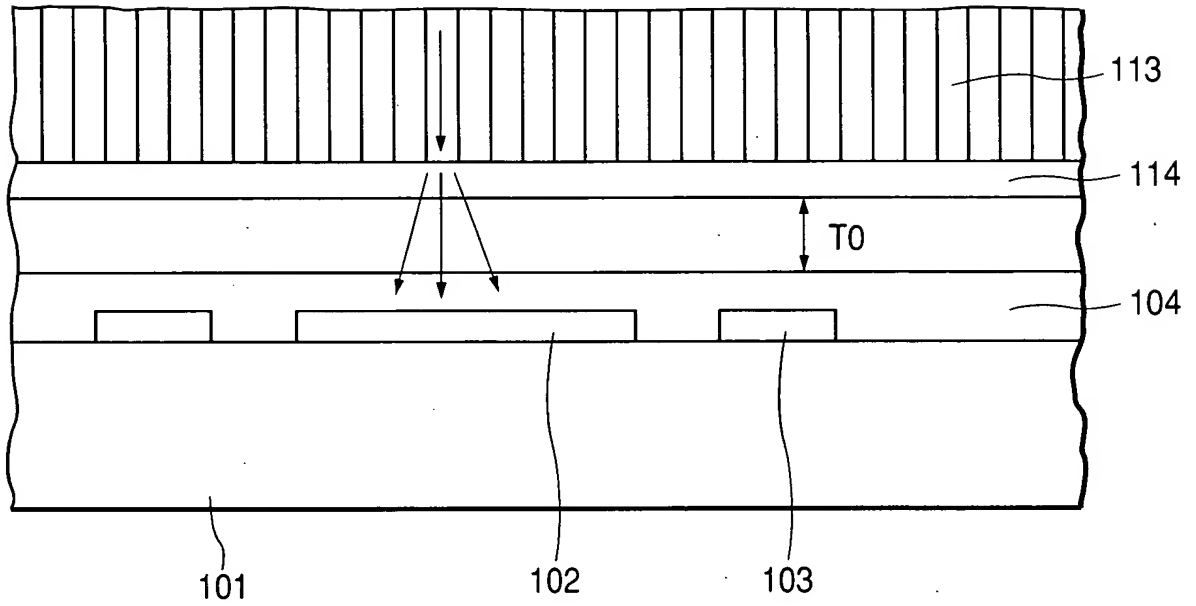
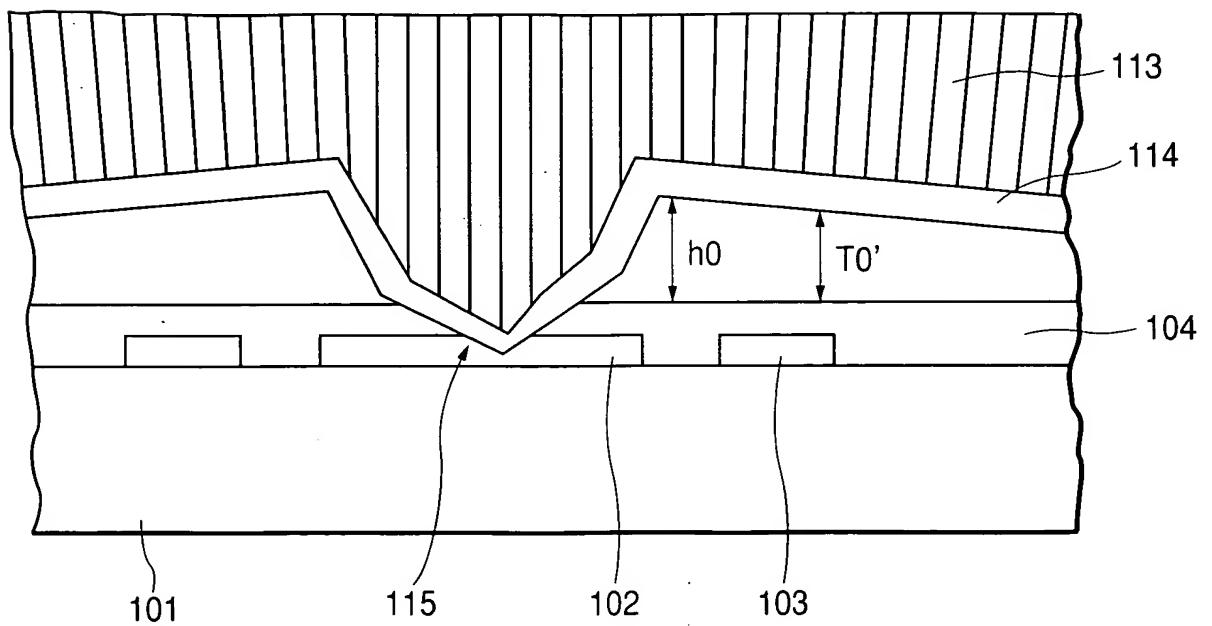


FIG. 16B



The diagram illustrates a 3-bit digital-to-analog converter (DAC) circuit. It features a 3x3 array of transistors and capacitors. The top row of transistors is connected to a common input line 510. The middle row of transistors is connected to a common input line 507. The bottom row of transistors is connected to a common input line 505. Each transistor in the array has a gate connected to a common input line 503. The sources of the transistors are connected to a common output line 506. The drains of the transistors are connected to a common output line 504. The output line 504 is connected to a 3-bit digital-to-analog converter 509. The output line 506 is connected to a 3-bit digital-to-analog converter 509. The output line 504 is also connected to a 3-bit digital-to-analog converter 509. The output line 506 is also connected to a 3-bit digital-to-analog converter 509. The output line 504 is also connected to a 3-bit digital-to-analog converter 509. The output line 506 is also connected to a 3-bit digital-to-analog converter 509.

The circuit diagram shows a 3x3 array of current sources. Each current source is formed by a PMOS transistor (top) and an NMOS transistor (bottom). The PMOS transistors are connected to a common supply rail (610). The NMOS transistors are connected to a common output rail (609). The gates of the PMOS transistors are connected to a 3-bit digital input (607). The gates of the NMOS transistors are connected to a 3-bit digital input (608). The output of the DAC is a 3-bit digital signal (609) that is connected to a 3-bit digital input (608) via a 3-bit digital-to-analog converter (604). The DAC is implemented using a 3x3 array of current sources, where each current source is formed by a PMOS transistor (top) and an NMOS transistor (bottom). The PMOS transistors are connected to a common supply rail (610). The NMOS transistors are connected to a common output rail (609). The gates of the PMOS transistors are connected to a 3-bit digital input (607). The gates of the NMOS transistors are connected to a 3-bit digital input (608). The output of the DAC is a 3-bit digital signal (609) that is connected to a 3-bit digital input (608) via a 3-bit digital-to-analog converter (604).

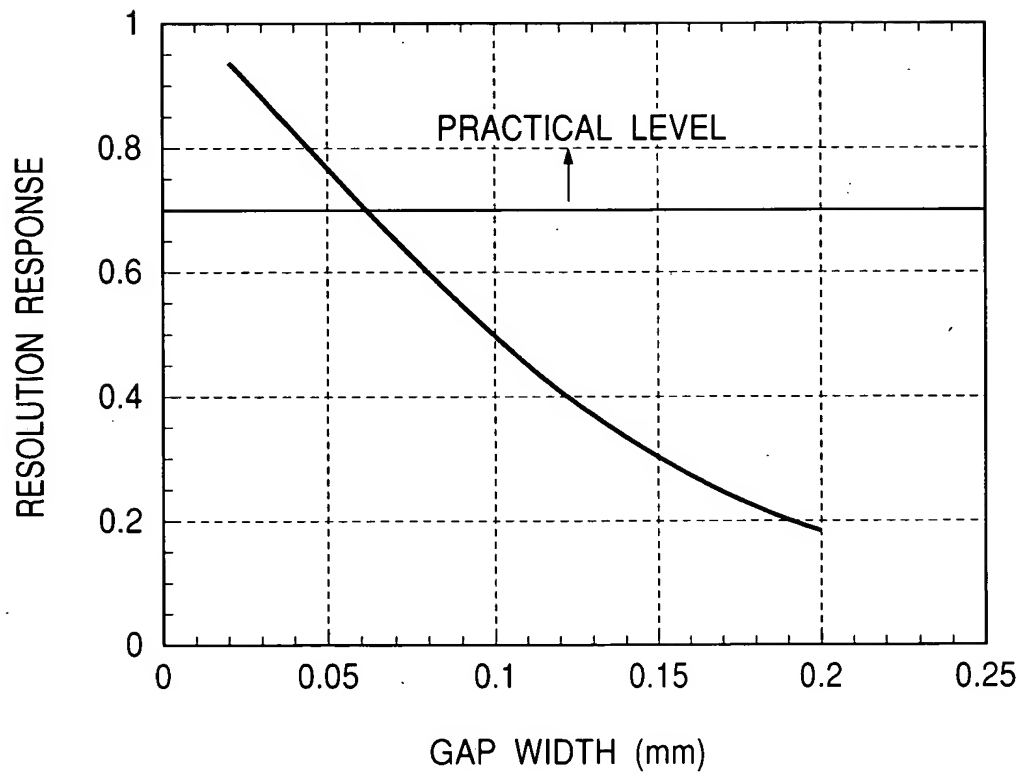
FIG. 19

FIG. 20A

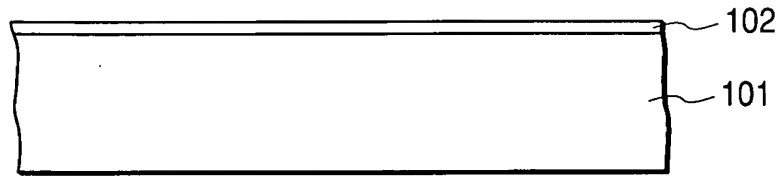


FIG. 20B

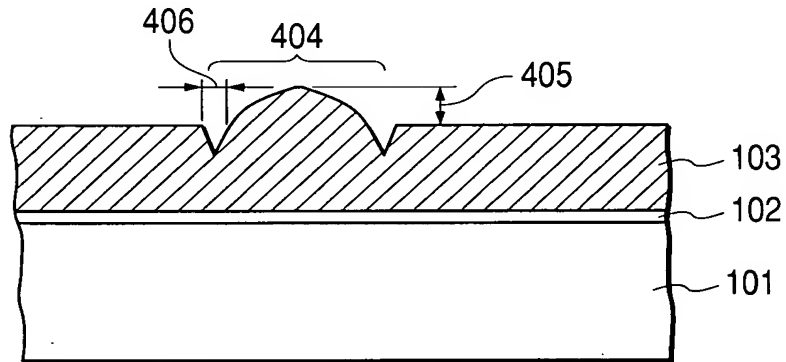


FIG. 20C

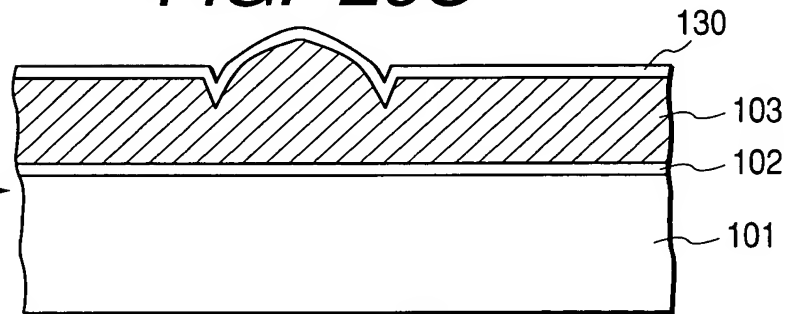


FIG. 20D

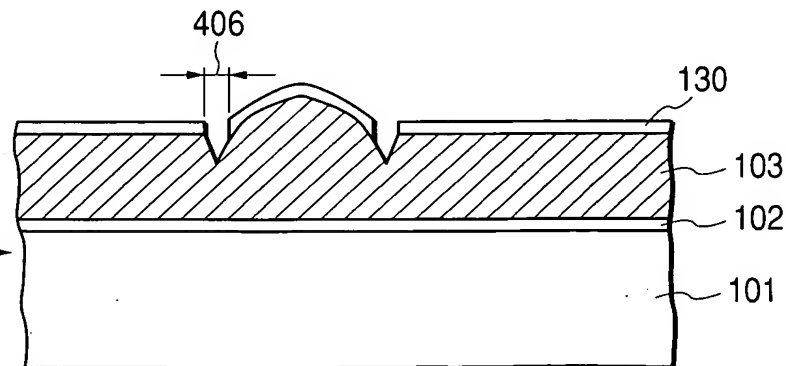


FIG. 21A

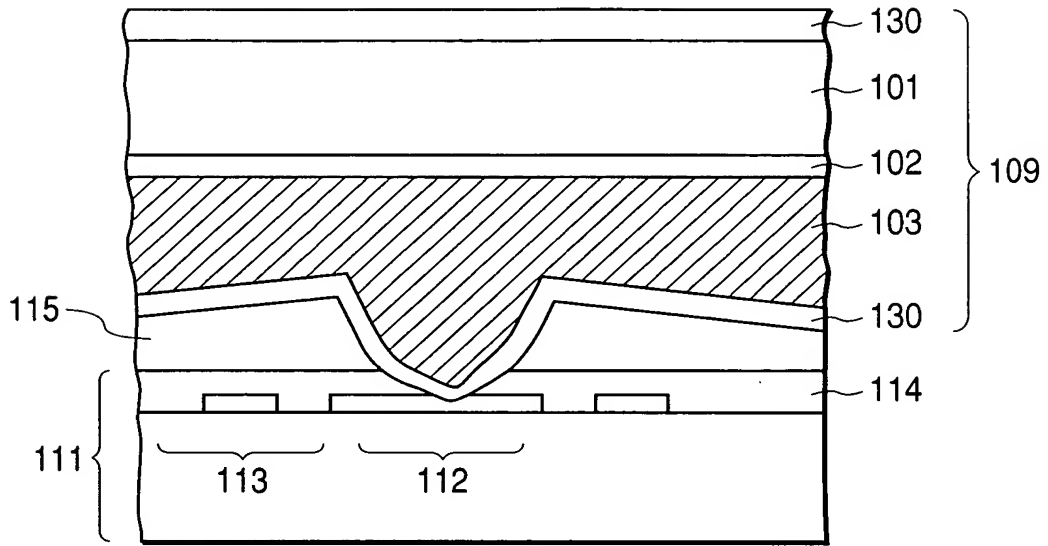


FIG. 21B

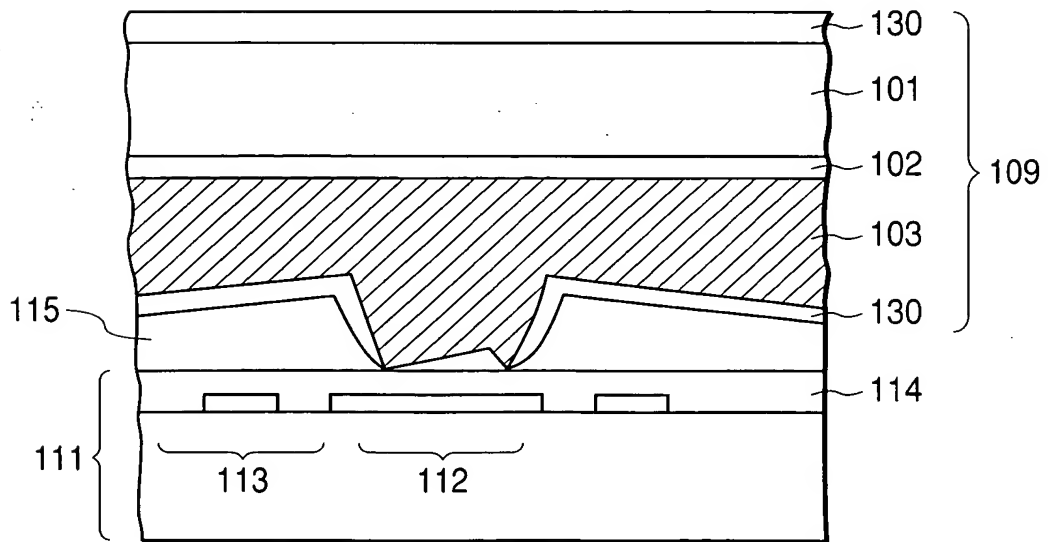


FIG. 22A

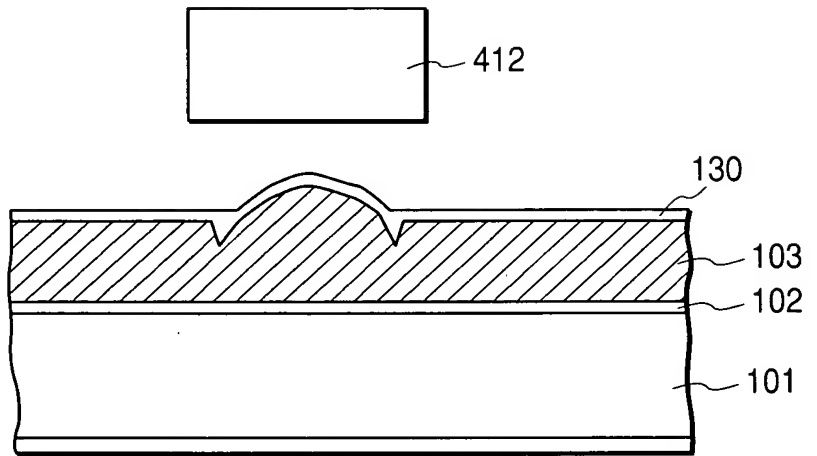


FIG. 22B

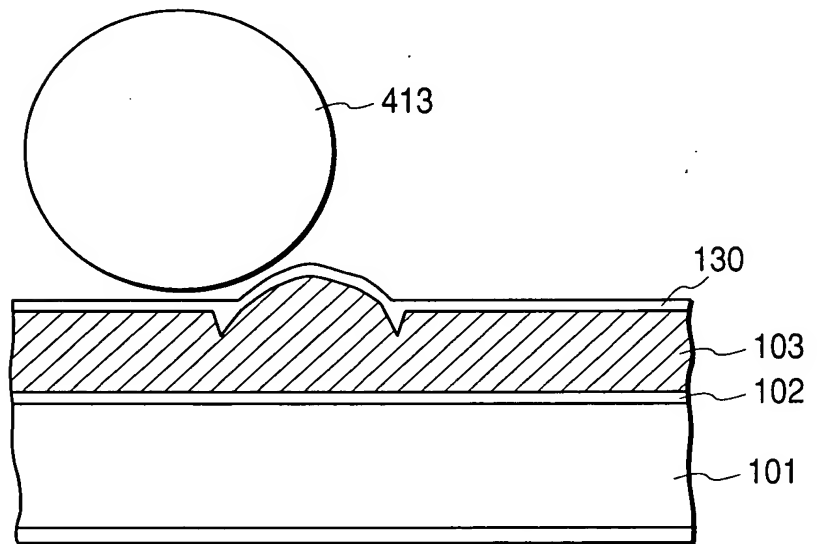


FIG. 22C

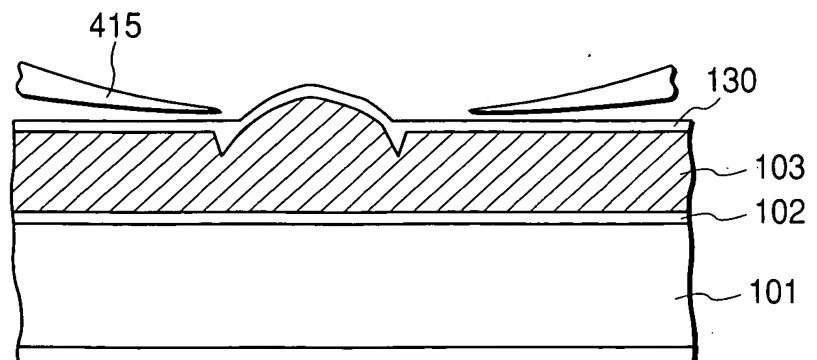


FIG. 23A

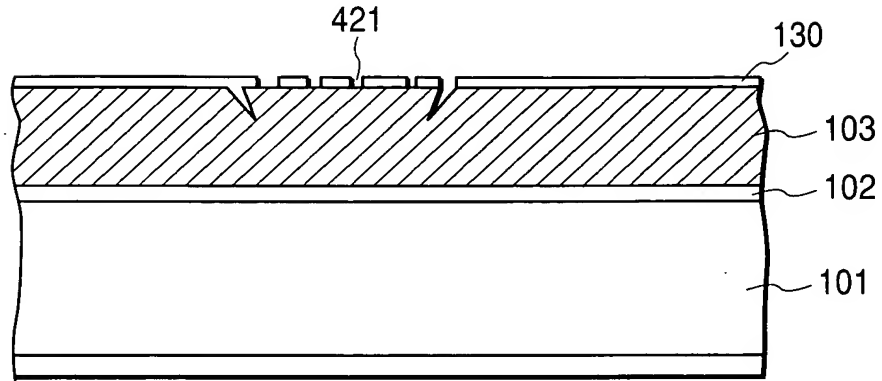


FIG. 23B

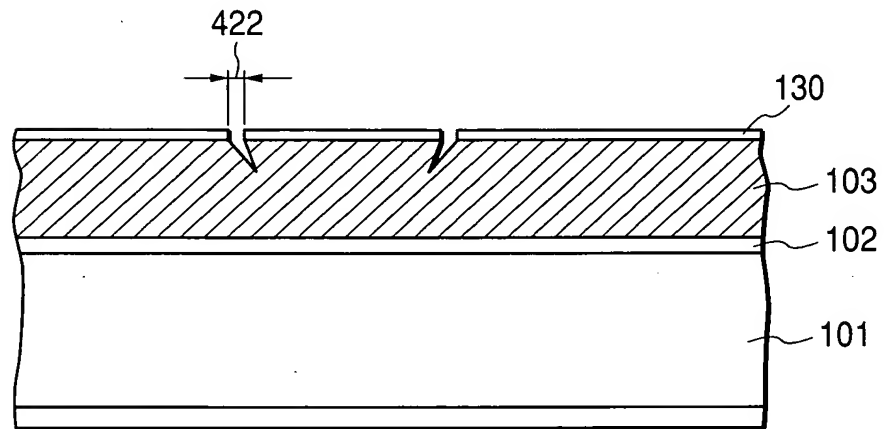


FIG. 23C

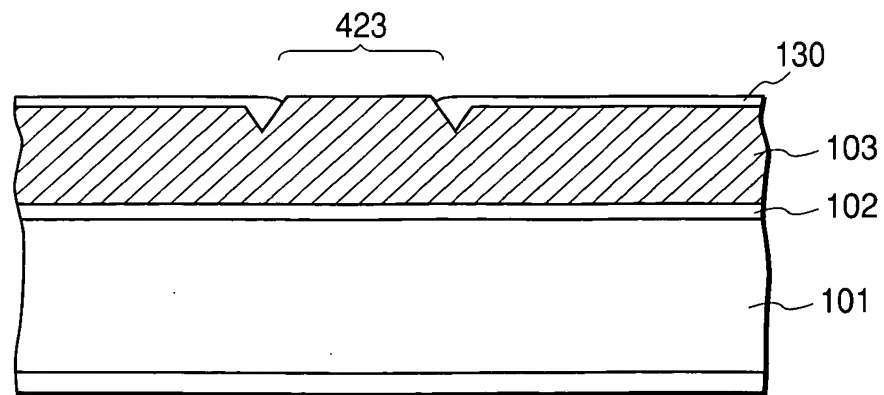


FIG. 24A

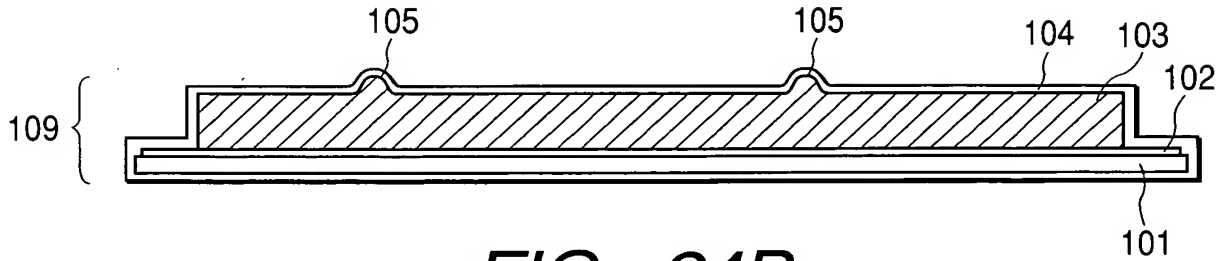


FIG. 24B

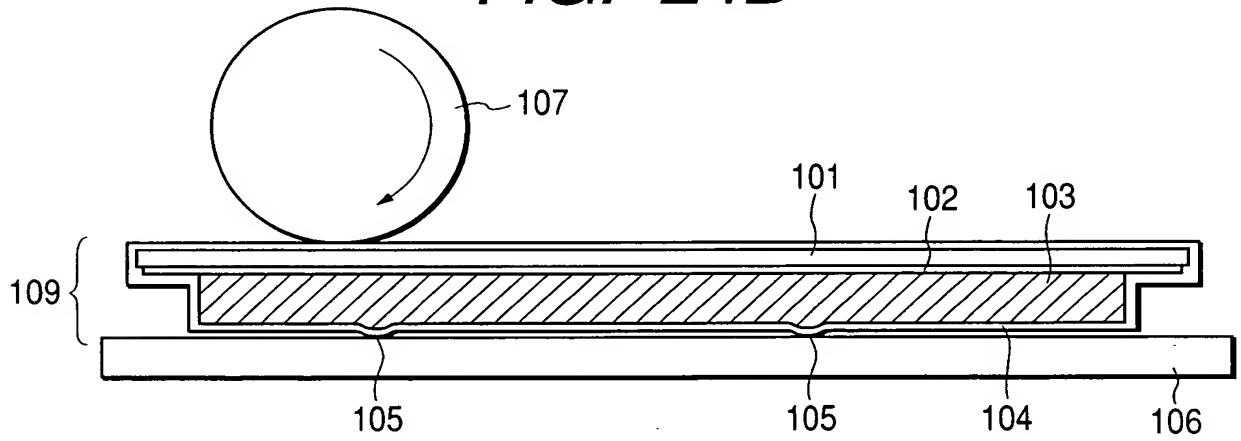


FIG. 24C

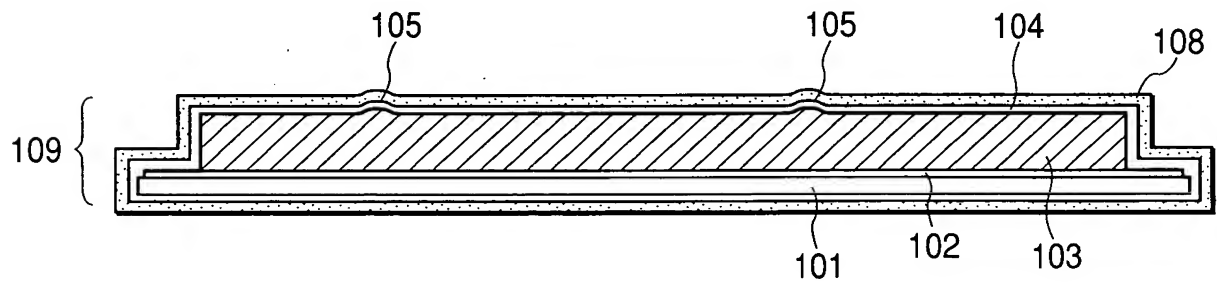


FIG. 24D

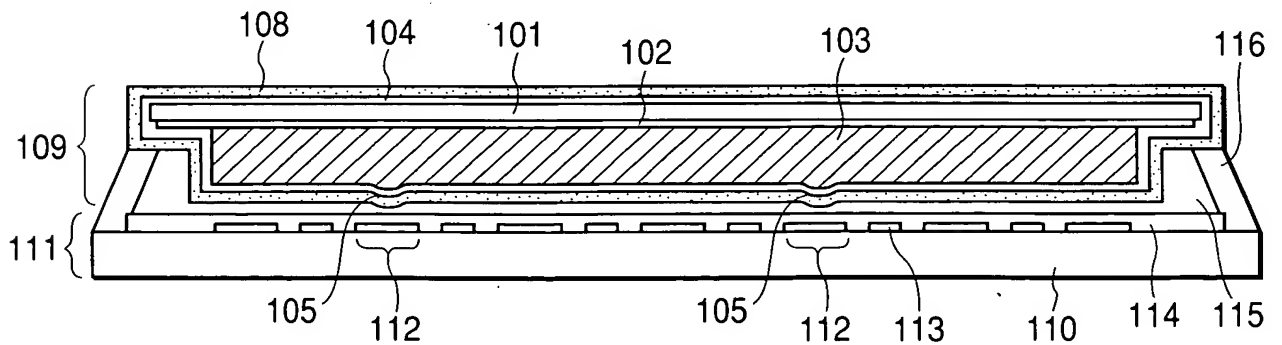


FIG. 25A

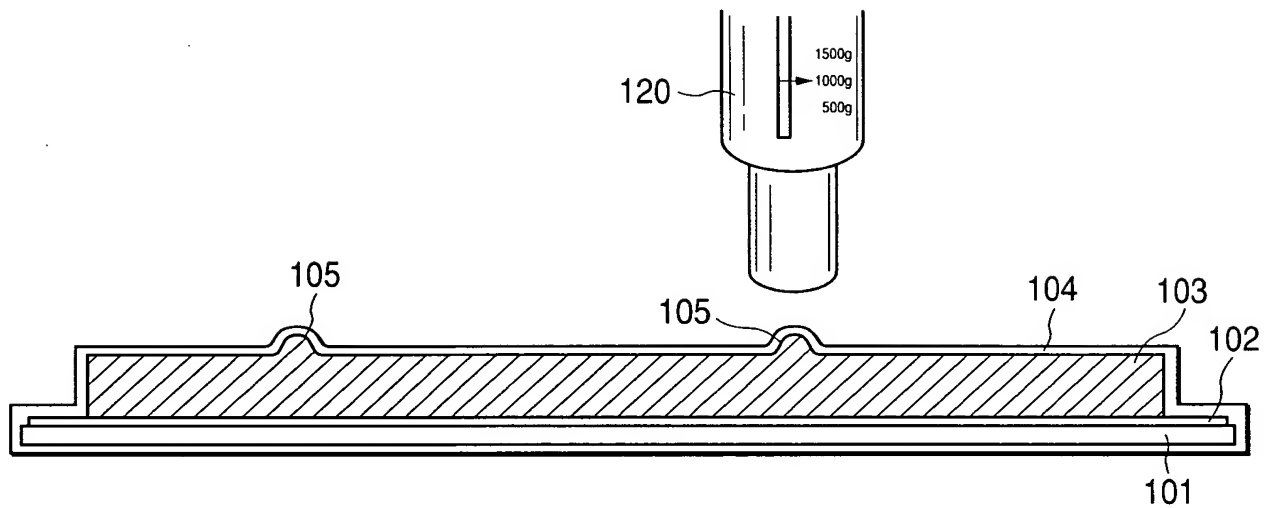
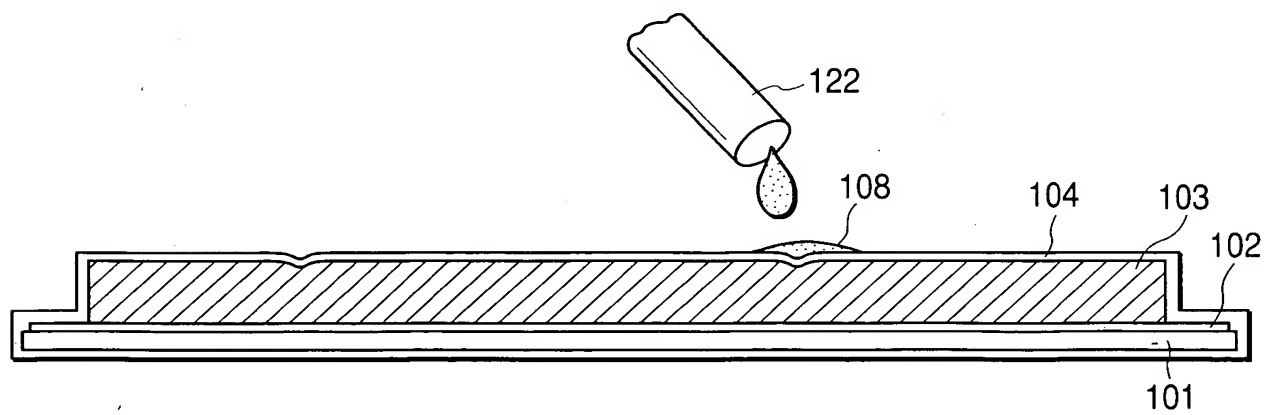


FIG. 25B



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FIG. 26

